

CURRICULUM VITAE

Part A. PERSONAL INFORMATION		CV date	13/09/2020
First and Family name	Yao-Ming Kuo		
Researcher codes	WoS Researcher ID	AAH-1322-2020	
	SCOPUS Author ID	57203020852	
	Open Researcher and Contributor ID (ORCID)	0000-0001-9752-6073	

A.1. Current position

Name of University/Institution	Universidad Antonio de Nebrija		
Department	ARIES Research Center		
Address and Country	C/ Pirineos 55, Madrid		
Phone number	+34 914521100	E-mail	ykuo@alumnos.nebrija.es
Current position	PhD Student	From	19/12/2019
Key words	ASIC, FPGA, Digital Design, Reliability		

A.2. Education

PhD	University	Year
Industrial and Computer Science Technology	Universidad Antonio de Nebrija	

A.3. JCR articles, h Index, thesis supervised...

Information from Google Scholar:

- Sum of times cited: 6
- Total number of publications: 4
- h Index: 2

Part B. CV SUMMARY (max. 3500 characters, including spaces)

Yao-Ming Kuo received the M.Eng. degree in Electronic Engineering (2018) from Universidad Tecnológica Nacional (UTN) in Buenos Aires, Argentina. He has previously worked as a researcher at UTN and as a Hardware Design Engineer at INTI (National Institute of Industrial Technology, Argentina) in the Micro and Nanoelectronics department.

He has designed ASIC, FPGA and Microcontroller based systems for instrumentation sensors and power-save applications at INTI for third party companies. He is currently a Ph.D. student in the ARIES Research Center with the aim of completing his thesis in 2022. His interest areas include Digital Signal Processing, Digital Design and Implementation, Fault-tolerance and Reliability.

Part C. RELEVANT MERITS

C.1. Publications (including books)

[1] Y.-M. Kuo, L. J. Arana, L. Seva, C. Marchese, and L. Tozzi, "Educational design kit for synopsys tools with a set of characterized standard cell library," in 2018 IEEE 9th Latin American Symposium on Circuits & Systems (LASCAS), Puerto Vallarta, Feb. 2018, pp. 1–4.

[2] Y.-M. Kuo, A. Grosso, F. Galimberti, J. Tantera, J. Mallo, and S. Verrastro, "Analog front-end design of contactless RFID smart card ISO/IEC14443A standard — Compliant," in 2018 IEEE 9th Latin American Symposium on Circuits & Systems (LASCAS), Puerto Vallarta, Feb. 2018, pp. 1–4.

[3] M. Prieto et al., "Evaluation of the uModel factory software used for the modeling of embedded systems with concurrent states," in 2017 IEEE 8th Latin American Symposium on Circuits & Systems (LASCAS), Bariloche, Feb. 2017, pp. 1–4.

[4] N. Gonzalez et al., "Evaluación del software uModelFactory como herramienta didáctica," in 2016 IEEE Biennial Congress of Argentina (ARGENCON), Buenos Aires, Argentina, Jun. 2016, pp. 1–5.

C.2. Research projects and grants

C.3. Contracts

C.4. Patents

C.5. International mobility

- University: TU Ilmenau
City: Ilmenau, Thüringen, Germany
Starting Date: 01/09/2014
Duration (months): 6
Topic: Präzisionstechnik und Präzisionsmesstechnik (DAAD Internship)