

CURRICULUM VITAE

Part A. PERSONAL INFORMATION		CV date	21/09/2020
First and Family name	Kyle Gear		
Researcher codes	WoS Researcher ID		
	SCOPUS Author ID		
	Open Researcher and Contributor ID (ORCID)	0000-0001-5411-524X	

A.1. Current position

Name of University/Institution	Universidad Antonio de Nebrija		
Department	ARIES Research Center		
Address and Country	55, Calle Pirineos, Madrid, Spain		
Phone number	+34914521100	E-mail	kgear@nebrija.es
Current position	PhD Student	From	18/1/2019
Key words	Computer and Electronics Engineering		

A.2. Education

PhD	University	Year
Master of Engineering (MEng)	Aston University, UK	2018
PhD	Universidad Nebrija	2022

A.3. JCR articles, h Index, thesis supervised...

- JCR articles: 1 (1-Q3).

Part B. CV SUMMARY (max. 3500 characters, including spaces)

Received the MEng degree in Electronic and Electrical Engineering (2018) from Aston University in Birmingham, UK. He has previously worked at Caterpillar Inc. Electronics European Department in the Quality, Reliability and Product Support team as an annual placement student. He is currently a PhD student in the ARIES Research Centre with the aim of completing in 2022. His interest areas include Digital Design, Signal Processing, Fault-tolerance and Reliability. Current research topics include investigations in the fault tolerance of CIC filters and in configuration memory scrubbers for FPGAs.

Part C. RELEVANT MERITS

C.1. Publications (including books)

JCR articles

1. **K.W. Gear**, A. Sánchez-Macián, F. Garcia-Herrero and Juan Antonio Maestro, "Two Behavioural Error Detection Techniques for the Cascaded Integrator-Comb Interpolation Filter Implemented on FPGA," *Circuits Syst Signal Process* 39, 5529–5542 (2020). <https://doi.org/10.1007/s00034-020-01418-6> **Q3**

C.2. Research projects and grants

C.3. Contracts

C.4. Patents

C.5, C.6, C.7... (e. g., Institutional responsibilities, memberships of scientific societies...)

Member of the IET